

## WEST Search History

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DATE: Wednesday, December 01, 2004

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
		<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L49	index register\$1 with zero	8
<input type="checkbox"/>	L48	index register\$1 with zero with flag	1
		<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L47	(index register\$1 with zero).ti,ab,clm.	13
<input type="checkbox"/>	L46	L45 not L43	10
<input type="checkbox"/>	L45	index register\$1 with zero with flag	13
<input type="checkbox"/>	L44	index register\$1 with zero	256
<input type="checkbox"/>	L43	index register\$1 with (zeroflag or zero flag\$1)	3
<input type="checkbox"/>	L42	operand address with zero	59
<input type="checkbox"/>	L41	operand address with (zeroflag or zero flag)	0
		<i>DB=USPT; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L40	US-5774687-A.did.	1
		<i>DB=EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L39	operand address with zero	7
		<i>DB=JPAB,EPAB,DWPI,TDBD; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L38	address registers and L37	8
<input type="checkbox"/>	L37	address mode\$1	499
<input type="checkbox"/>	L36	operand memories	5
<input type="checkbox"/>	L35	operand address registers	16
		<i>DB=USPT,PGPB; PLUR=NO; OP=ADJ</i>	
<input type="checkbox"/>	L34	L33 not L30	32
<input type="checkbox"/>	L33	L31 and L32	36
<input type="checkbox"/>	L32	L27 and decrement\$3	39
<input type="checkbox"/>	L31	L27 and increment\$3	76
<input type="checkbox"/>	L30	L27 and L29	11
<input type="checkbox"/>	L29	(((711/219)!.CCLS.))	341
<input type="checkbox"/>	L28	L26 and L27	0
<input type="checkbox"/>	L27	(((711/110)!.CCLS.))	171
<input type="checkbox"/>	L26	(((708/672)!.CCLS.))	104
<input type="checkbox"/>	L25	3331056.pn.	1
<input type="checkbox"/>	L24	L20 not L23	170

<input type="checkbox"/>	L23	L20 and L22	16
<input type="checkbox"/>	L22	((711/214)!.CCLS.)	145
<input type="checkbox"/>	L21	L15 and L20	16
<input type="checkbox"/>	L20	((711/212)!.CCLS.)	186
<input type="checkbox"/>	L19	L15 with bit\$1	644
<input type="checkbox"/>	L18	L15 and L17	3
<input type="checkbox"/>	L17	operand address registers	35
<input type="checkbox"/>	L16	L15 same L12	2
<input type="checkbox"/>	L15	address mode\$1	2465
<input type="checkbox"/>	L14	L9 same L12	45
<input type="checkbox"/>	L13	L9 and L12	109
<input type="checkbox"/>	L12	4-bit address\$2	797
<input type="checkbox"/>	L11	L9 same L10	59
<input type="checkbox"/>	L10	16-bit address\$2	1544
<input type="checkbox"/>	L9	8-bit address\$2	1388
<input type="checkbox"/>	L8	operand memories	8
<input type="checkbox"/>	L7	multiple operand memories	0
<input type="checkbox"/>	L6	multiple	999944
<input type="checkbox"/>	L5	multiple	999944
<input type="checkbox"/>	L4	L3 same operand memory	4
<input type="checkbox"/>	L3	operand address register	148
<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>			
<input type="checkbox"/>	L2	operand address\$3 with L1	21
<input type="checkbox"/>	L1	index registers	1181

END OF SEARCH HISTORY